



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD HYDERABAD - 500 085, TELANGANA STATE, INDIA.



CONSOLIDATED MARKS MEMO / CREDIT SHEET

B.Tech. ELECTRICAL & ELECTRONICS ENGINEERING



CMM No.: **c 0591729**

Serial No.: 21132019387

Name: **BODA AJAY**

Name of the College: QK-JITS, NUSTULAPUR

Month & Year of Final Exam: May, 2015

Hall Ticket No.: **11QK1A0242**

Year of Admission: 2011-2012

Class Awarded: FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	19	60	79	4	2	MATHEMATICS - I	19	26	45	6
3	MATHEMATICAL METHODS	20	38	58	6	4	ENGINEERING PHYSICS	18	38	56	4
5	ENGINEERING CHEMISTRY	18	26	44	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	22	49	71	6
7	ENGINEERING DRAWING	22	26	48	4	8	COMPUTER PROGRAMMING LAB	23	45	68	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	20	43	63	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	20	37	57	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	21	42	63	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS-III	20	35	55	3	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	22	33	55	4
2	FLUID MECHANICS AND HYDRAULIC MACHINERY	18	30	48	3	2	POWER SYSTEMS - I	17	50	67	3
3	ELECTRONIC DEVICES & CIRCUITS	20	36	56	4	3	ELECTRONIC CIRCUITS	22	31	53	3
4	ELECTRICAL CIRCUITS	17	42	59	4	4	SWITCHING THEORY & LOGIC DESIGN	18	40	58	4
5	ELECTRO MAGNETIC FIELDS	21	27	48	3	5	NETWORK THEORY	18	45	63	3
6	ELECTRICAL MACHINES-I	17	26	43	4	6	ELECTRICAL MACHINES - II	16	26	42	4
7	FLUID MECHANICS AND HYDRAULIC MACHINERY LAB	21	43	64	2	7	ELECTRICAL MACHINES LAB - I	21	41	62	2
8	ELECTRONIC DEVICES & CIRCUITS LAB	23	45	68	2	8	ELECTRIC CIRCUITS AND SIMULATION LAB	18	42	60	2
I SEMESTER											
III YEAR											
1	IC APPLICATIONS	22	41	63	3	1	ELECTRICAL MEASUREMENTS	22	32	54	3
2	MANAGEMENT SCIENCE	24	33	57	3	2	COMPUTER METHODS IN POWER SYSTEMS	21	37	58	4
3	POWER SYSTEMS - II	21	31	52	4	3	ENVIRONMENTAL STUDIES	24	35	59	3
4	CONTROL SYSTEMS	19	32	51	3	4	MICROPROCESSORS & MICROCONTROLLERS	22	39	61	4
5	POWER ELECTRONICS	20	42	62	4	5	POWER ELECTRONICS AND SIMULATION (LAB)	21	44	65	2
6	ELECTRICAL MACHINES - III	19	26	45	4	6	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	23	47	70	2
7	ELECTRICAL MACHINES - II (LAB)	21	46	67	2	7	RENEWABLE ENERGY SOURCES	22	55	77	3
8	CONTROL SYSTEMS AND SIMULATION (LAB)	22	46	68	2	8	POWER SEMICONDUCTOR DRIVES	21	63	84	4
I SEMESTER											
IV YEAR											
1	POWER SYSTEM OPERATION AND CONTROL	24	32	56	4	1	HVDC TRANSMISSION	23	42	65	3
2	HIGH VOLTAGE ENGINEERING	22	39	61	4	2	LINEAR SYSTEMS ANALYSIS	23	38	61	3
3	UTILIZATION OF ELECTRICAL ENERGY	22	40	62	3	3	EHV AC TRANSMISSION	24	44	68	3
4	INSTRUMENTATION	23	40	63	3	4	INDUSTRY ORIENTED MINI PROJECT	-	46	46	2
5	SWITCHGEAR AND PROTECTION	23	40	63	3	5	SEMINAR	47	-	47	2
6	ELECTRICAL MEASUREMENTS LAB	23	44	67	2	6	MAJOR PROJECT	40	142	182	10
7	ELECTRICAL DISTRIBUTION SYSTEMS	22	45	67	4	7	COMPREHENSIVE VIVA	-	95	95	2
8	MICROPROCESSORS AND MICROCONTROLLERS LAB	22	48	70	2						

(# Project Internal= 50, External=150)

Number of Credits registered for: 200 Aggregate Marks Secured for best: 192

Aggregate Marks Secured: 3534 OUT OF 5250 (67.31%)

Date of issue: June 15, 2015

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)



Ajay Kumar

CONTROLLER OF EXAMINATIONS