

Board of Secondary Education

ANDHRA PRADESH, INDIA

LL0660468



SECONDARY SCHOOL CERTIFICATE

REGULAR

PG/31/30496/0650988/7

CERTIFIED THAT BANOTH CHINTU KUMAR

FATHER NAME : BANOTH LAXMAN

MOTHER NAME: BANOTH SUBHADRA

bearing Roll No. 1231113867

belonging to A P T W RESIDENTIAL (B) EM WARANGAL

has appeared and PASSED SSC EXAMINATION held in MARCH 2012 with GPA 8.0

and ENGLISH as medium of instruction.

DATE OF BIRTH

19/10/1996

ONE NINE

OCTOBER

ONE NINE NINE SIX

THE CANDIDATE SECURED THE FOLLOWING GRADES AND GRADE POINTS

SUBJECT	GRADES	GRADE POINTS	
		In Figures	In Words
FIRST LANGUAGE : (TELUGU)	B1	08	ZERO EIGHT
THIRD LANGUAGE : ENGLISH	B2	07	ZERO SEVEN
MATHEMATICS :	A2	09	ZERO NINE
GENERAL SCIENCE :	A2	09	ZERO NINE
SOCIAL STUDIES :	A2	09	ZERO NINE
SECOND LANGUAGE : (HINDI)	C1	06	ZERO SIX
GRADE POINTS TOTAL :		48	FOUR EIGHT

Life Skills Education : GRADE SECURED :

Marks of Identification

1. A MOLE ON THE RIGHT SIDE OF THE NECK
2. A MOLE ON THE RIGHT HAND

Head of Institution
with School Stamp

PRINCIPAL

Date of Issue : 24th May 2012

A.P.T.W. RESIDENTIAL (BOYS)
ENGLISH MEDIUM SCHOOL
WARANGAL-506 013

SECRETARY
BOARD OF SECONDARY EDUCATION
A.P. HYDERABAD

1. Life skills Education : The Grade shall be supported by the respective Head of the Institution before delivery of the certificate.
2. Any corrections in the certificate will not be entertained after one year from the date of issue.
3. Any unauthorised correction in the certificate will result in cancellation of certificate.
4. The Grades with asterisk indicate the old grades secured in previous appearances.

2012



Sl. No. **N388652**

Board of Intermediate Education, A. P.

Vidya Bhavan, Nampally, Hyderabad - 500 001



INTERMEDIATE PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that **BANOTH CHINTU KUMAR**Father Name : **BANOTH LAXMAN**Mother Name : **BANOTH SUBHADRA**

bearing

Registered No. **1420212356**

has appeared at the Intermediate Public

Examination held in **MARCH-2014**and passed in **A GRADE**with **ENGLISH** as the medium of Instruction.

The subjects in which he was examined and the marks awarded are as follows :

Subject	I Year		II Year	
	Maximum Marks	Marks Secured	Maximum Marks	Marks Secured
Part - 1 : ENGLISH	100	078	100	084
Part - 2 : TELUGU	100	088	100	081
Part - 3 : Optional Subjects				
MATHEMATICS - A	075	060	075	075
MATHEMATICS - B	075	069	075	062
PHYSICS	060	054	060	045
CHEMISTRY	060	054	060	049
PHYSICS PRACTICAL			030	029
CHEMISTRY PRACTICAL			030	026
ENVIRONMENTAL EDUCATION	Q U A L I F I E D			
Total Marks In Figures	854 ➡ 854			
In words	*EIGHT**FIVE***FOUR*			
Date	03-05-2014			

Mandayala
Principal

T.T.W.R Jr. College of Excellence

Beside Kakatiya Canal, Peddammaipadu

Mulugu Road, WARANGAL - 506 001

Controller of Examinations

Signature of the Principal and Controller of Examinations Seal

OVERLEAF * INDICATES MARKS OBTAINED AT AN EARLIER EXAMINATION

1420212356



OSMANIA UNIVERSITY

CONSOLIDATED MEMORANDUM OF MARKS

EG 218548

EXAMINATION: B.E. (ELECTRONICS & COMM.) (2014-15 TO 2017-18)
MAY/JUNE 2018

REF NO.: 84
ROLL NO.: 245114735175 NAME: BANDITH CHINTU KUMAR

PARENT(S) NAME: BANDITH LAXMAN

ROLL NO. : 245114735175		U. EXAM		SESS.	
SL. NO.	SUBJECTS	MAX. MARKS	MARKS SECURED	MAX. MARKS	MARKS SECURED
I YEAR					
1	ENGLISH	75	46	25	13
2	MATHEMATICS - I	75	51	25	14
3	MATHEMATICS - II	75	37	25	11
4	ENGINEERING PHYSICS	75	38	25	12
5	PHYSICS LAB	50	38	25	21
6	ENGINEERING CHEMISTRY	75	30	25	11
7	CHEMISTRY LAB	50	40	25	19
II YEAR I SEMESTER					
1	APPLIED MATHEMATICS	75	30	25	16
2	BASIC CIRCUITS ANALYSIS	75	34	25	12
3	ELECTROMAGNETIC THEORY	75	42	25	16
4	ELECTRONIC DEVICES	75	32	25	8
5	ELEMENTS OF MECH. ENGINEERING	75	47	25	20
6	ELECTRICAL TECHNOLOGY	75	31	25	15
7	ELECTRONIC DEVICES - I LAB	50	29	25	20
8	ELECTRONIC W. & SIMULATION LAB	50	30	25	21
III YEAR I SEMESTER					
1	LINEAR INTEGRATED CIR. & APPS.	75	32	25	7
2	PULSE & DIGITAL CIRCUITS	75	31	25	11
3	ANALOG COMMUNICATION	75	32	25	9
4	AUTOMATIC CONTROL SYSTEMS	75	34	25	17
5	COMPUTER ORG. & ARCHITECTURE	75	33	25	11
6	D.S.D. WITH VERILOG HDL	75	30	25	8
7	PULSE DIG. & INTEGTD. CIRTS. LAB	50	27	25	19
8	VERILOG HDL LAB	50	32	25	18
IV YEAR I SEMESTER					
1	MICROWAVE ENGINEERING	75	34	25	11
2	VLSI DESIGN	75	34	25	12
3	ELECTRONIC INSTRUMENTATION	75	34	25	13
4	DIGITAL IMAGE PROCESSING	75	30	25	17
5	ENTREPRENEURSHIP	75	37	25	21
6	INDUSTRIAL ADMN. & FIN. MANG.	75	35	25	17
7	MICROWAVE ENGINEERING LAB	50	37	25	20
8	EMB. C AND VLSI DESIGN LAB	50	37	25	19
9	PROJECT SEMINAR	==	==	25	23

SL. NO.	SUBJECTS	U EXAM		SESS.	
		MAX MARKS	MARKS SECURED	MAX MARKS	MARKS SECURED
I YEAR					
8	ENGINEERING GRAPHICS	100	41	50	33
9	WORKSHOP PRACTICE	50	42	25	23
10	ENGINEERING MECHANICS	75	38	25	13
11	PROGRAMMING IN C & C++	75	36	25	12
12	PROGRAMMING LAB	50	43	25	21
13	ENGLISH LANGUAGE LAB	50	44	25	21
II YEAR II SEMESTER					
1	ANALOG ELECTRONICS CIRCUITS	75	30	25	12
2	NETWORKS & TRANSMISSION LINES	75	46	25	17
3	PROBABILITY THEORY AND S.P.	75	53	25	13
4	SIGNAL ANA. & TRANSFORM TECH.	75	38	25	12
5	SWITCHING THEORY & LOGIC DESIGN	75	39	25	12
6	ENVIRONMENTAL STUDIES	75	61	25	10
7	A.E.C./E.C LAB	50	29	25	16
8	ELECTRICAL TECHNOLOGY LAB	50	37	25	19
III YEAR II SEMESTER					
1	DIGITAL COMMUNICATION	75	32	25	17
2	DIGITAL SIGNAL PROCESSING	75	38	25	15
3	ANTENNAS AND WAVE PROPAGATION	75	33	25	11
4	MICROP. & MICROCONTROLLERS	75	30	25	11
5	MANAGERIAL ECONOMICS & ACCY.	75	34	25	23
6	COMMUNICATION LAB	50	38	25	24
7	SYSTEMS AND SIGNAL PRO. LAB	50	40	25	18
8	MPMC LAB	50	38	25	20
9	INDUSTRIAL VISIT / STUDY	==	==	GRD	60
IV YEAR II SEMESTER					
1	DATA COMM. COMP. NETWORKS	75	41	25	17
2	RADAR SYSTEMS	75	41	25	12
3	DISASTER MITIGATION AND MANG.	75	35	25	13
4	PROJECT	GRD	SAT	50	44
5	SEMINAR	==	==	25	20

Note: Bridge course I&II Sems subjects marks are not included in the Aggregate

AGGREGATE: 3022 AGGREGATE IN WORDS: ** THREE ZERO TWO TWO **

G.Total: 5350

DIVISION SECOND

[Signature]
CONTROLLER OF EXAMINATIONS

SECTION IN CHARGE

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